## Analog Computer Applications

The ChuA-oscillator ${ }^{1}$

## 1 Introduction

Although unintended, it turned out that the first three issues of "Analog Computer Applications" are solely devoted to chaotic systems and their implementation on analog computers. This third issue deals with the so-called Chua-oscillator. This circuit was discovered in 1983 by Leon O. Chua ${ }^{2}$ and is a classic example of an electronic circuit exhibiting chaotic behavior. This oscillator generates a rather unique and beautiful attractor (the Double Scroll attractor). It is based on a (rather hypothetical) nonlinear device called ChUA-diode which can be implemented by means of a small operational amplifier circuit.

The mathematical description of this particular oscillator is based on three coupled differential equations of the form

$$
\begin{align*}
\dot{x} & =c_{1}(y-x-f(x))  \tag{1}\\
\dot{y} & =c_{2}(x-y+z)  \tag{2}\\
\dot{z} & =-c_{3} y, \tag{3}
\end{align*}
$$

where $f(x)$ describes the behavior of the ChUA-diode and is traditionally defined as

$$
\begin{equation*}
f(x)=m_{1} x+\frac{m_{0}-m_{1}}{2}(|x+1|-|x-1|) . \tag{4}
\end{equation*}
$$

[^0]Dr. Bernd Ulmann, Issue \#3, 14-SEP-2016

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The standard values for the parameters are

$$
\begin{aligned}
c_{1} & =15.6 \\
c_{2} & =1 \\
c_{3} & =28 \\
m_{0} & =-1.143 \\
m_{1} & =-0.714
\end{aligned}
$$

## 2 Scaling

To implement this system of coupled differential equations on a traditional analog computer, all of its terms must be scaled so that

- no computing element will be driven into overload, so no term may exceed $\pm 1$ machine unit and
- every computing element should be driven over as much of its voltage range as possible to minimize the influence of component errors etc.

This task is not trivial due to the nonlinear elements in these coupled differential equations. By computing example solutions on a digital computer - a technique that was already used in the 1960s - the ranges of the various terms was determined which were then used for guidance during the process of scaling.

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The above equations were split into individual terms resulting in the following set of scaled equations. (1) is replaced by

$$
\begin{align*}
x_{0} & =0.1 \text { (initial condition for the integration) }  \tag{5}\\
x_{1} & =-10(x+f(x))  \tag{6}\\
x_{2} & =y+\frac{1}{2} x_{1}  \tag{7}\\
x & =3.12 \int x_{2} \mathrm{~d} t+x_{0} . \tag{8}
\end{align*}
$$

(2) is transformed into

$$
\begin{align*}
y_{1} & =z-\frac{1}{8} y  \tag{9}\\
y_{2} & =1.25 x+2 y_{1}  \tag{10}\\
y & =4 \int y_{2} \mathrm{~d} t, \tag{11}
\end{align*}
$$

while (3) becomes

$$
\begin{equation*}
z=-\int 3.5 y \mathrm{~d} t \tag{12}
\end{equation*}
$$

The central element, the ChuA-diode is a bit nasty to implement as the absolute value function requires at least two diodes and an open amplifier for a proper implementation. ${ }^{3} f(x)$ is broken down and scaled into

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Figure 1: Partial computer setup for equations (5, 6, 7, 8)

$$
\begin{align*}
f_{1} & =|0.7143 x+0.2857|  \tag{13}\\
f_{2} & =|0.7143 x-0.2857|  \tag{14}\\
f_{3} & =f_{1}-f_{2}  \tag{15}\\
f(x) & =-0.714 x-0.3003 f_{3} \tag{16}
\end{align*}
$$

## 3 Computer setup

Deriving a computer setup from these equations is rather straight-forward. ${ }^{4}$
The first set of equations, $(5,6,7,8)$, is implemented as shown in figure 1 . The equations $(9,10,11,12)$ are implemented as shown in figure 2.

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Figure 2: Partial computer setup for equations ( $9,10,11,12$ )
The mechanization of the central function $f(x)$ is a bit awkward as two absolute value functions are required. The setup representing equations ( $13,14,15,16$ ) is shown in figure 3.

Figure 4 shows the program implemented on an early prototype of Analog Paradigm's Model-1 analog computer which features an absolute value function module, significantly simplifying the implementation of $f(x)$. In this case the summers $\mathrm{S} 12, \mathrm{~S} 13, \mathrm{~S} 14$, and S 15 as well as the four free diodes can be replaced by two absolute value function modules.

## 4 Results

By changing the time-constant of the three integrators of this circuit, it is easily possible to either display the Double Scroll Attractor on an oscilloscope with $x$ - and $y$-inputs (driven by $x$ and $y$ of the computer circuit) or to plot the attractor using a traditional pen plotter. Figure 5 shows a screen-shot from an oscilloscope's screen, generated on the Analog Paradigm Model-1 analog computer with the integrator time

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Figure 3: Partial computer setup for equations $(13,14,15,16)$
constants set to 1000 . Using slower time constants output on a plotter is, of course, also possible.
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Figure 4: Setup of the ChUA-oscillator on an early Analog Paradigm Model-1 analog computer

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Figure 5: Phase space display of the Double Scroll attractor generated on an Analog Paradigm Model-1 analog computer


[^0]:    ${ }^{1}$ The author would like to thank Mr. David Broadbent for his valuable suggestions.
    ${ }^{2}$ *June 28, 1936

[^1]:    ${ }^{3} \mathrm{All}$ in all four external diodes are required to implement the two absolute value functions.

[^2]:    ${ }^{4}$ The numbers of the computing elements shown in the following were originally geared towards a Telefunken RA 742 table-top analog computer and can be ignored if another machine is going to be used. Inputs denoted with a 10 are weighted inputs with a gain of 10 which is essential for the scaling to work.

